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Low-power FSM synthesis Even for designs containing a good Finite-state machines (FSMs) are widely used to implement control logic in various applications such as microprocessors, digital transmission, digital filters and digital signal processing. The synthesis method must be able to reduce both dynamic power and leakage. Thus, power-efficient synthesis of FSM has come up as a very important problem domain, attracting a lot of research. As a result, the controller consumes a good amount of system power. in various applications such as microprocessors, digital transmission, digital filters and digital signal processing. As the devices are mostly portable and hand-held, reducing power dissipation has emerged as the primary concern of today's VLSI designers. While the datapath elements can be shut down when they are not being used, controllers are always active. Even for designs containing a good number of datapath elements, the controller occupies a sizeable portion cbfabd5e0b

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